IN THE SPECIFICATION:

The specification as amended below with replacement paragraphs shows added text with <u>underlining</u> and deleted text with ~~strikethrough~~.

Please REPLACE the paragraph beginning at line 1 on page 17 with the following paragraph:

In Figure 8, the "~~write~~<u>read</u>" addressing mechanism of station 4 is illustrated. User address 1 indicates that this interface module 18 is parametrized as transceiver. In addition, in one part of read-write memory 16, the read authorization of this station 4 on the serial ring bus is stored in the form of a table. In this table are entered all message addresses, including their address AH and their subaddress AS, which are permitted to be read by this transceiver station 4. In reading these messages CT, the data, here acceleration setpoint value a_{soll}, of data field DF of container message CT are copied, in accordance with subaddress AS, into the corresponding data channel of the receiving memory of read-write memory 16. A comparison with container messages CT of dispatcher station 2 in accordance with Figure 7 indicates that the message having address 0/1 may not be read by transceiver station 4, i.e., this message having address 0/1 is transmitted only with the assistance of interface circuit 50 of this interface module 18 to the next user of this serial ring bus.